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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Huttner et al.
Appl. No. : 09/313,424
Filed : May 17, 1999
Title : SOI Semiconductor Configuration And Method
Of Fabricating The Same
Examiner : Brook Kebede
Group Art Unit : 2823

REQUEST TO APPROVE DRAWING CHANGES

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Please approve the newly entered drawings of Figs. 5a-5e.

Respectfully submitted,

Handwritten signature of Markus Nollf

For Applicants

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Date: January 11, 2002

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Fig. 5a

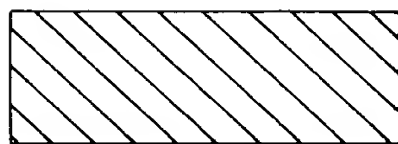


Fig. 5b

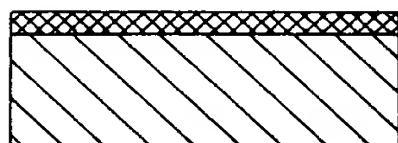


Fig. 5c

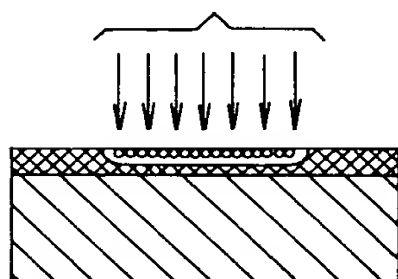


Fig. 5d

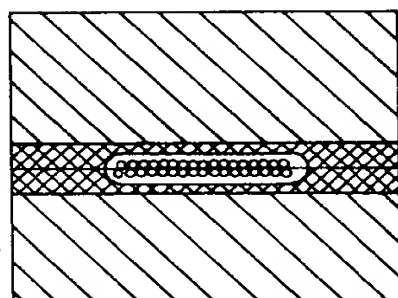


Fig. 5e

